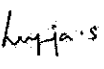

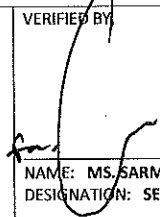




SCHOOL OF ENGINEERING AND BUILT ENVIRONMENT
SEMESTER FINAL EXAMINATION SCHEDULE

DEPARTMENT:	ELECTRICAL AND ELECTRONICS ENGINEERING
PROGRAM NAME:	MASTER OF SCIENCE IN ELECTRICAL AND ELECTRONICS ENGINEERING
INTAKE:	2511 (3 Students)
SEMESTER:	NOV 2025
ACADEMIC SESSION:	24 NOV 2025 – 3 APRIL 2026

Date/Day	TIME	SUBJECT CODE	SUBJECT NAME	EXAM HALL NO
MONDAY 30.03.2026	09.30 AM -12.30 PM	MEE 1133	ADVANCED MICROCONTROLLER	TBA
WEDNESDAY 01.04.2026	09.30 AM -12.30 PM	MEE 1123	ADVANCED VLSI DESIGN	TBA
FRIDAY 03.04.2026	09.30 AM -12.30 PM	MET 1233	IMAGE PROCESSING	TBA
MONDAY 06.04.2026	09.30 AM -12.30 PM	MET 1213	ELECTRIC POWER DISTRIBUTION	TBA
WEDNESDAY 08.04.2026	09.30 AM -12.30 PM	MEE 1223	POWER ELECTRONICS	TBA

PREPARED BY,  NAME: MS. SUPRAJA SRINIVASAN DESIGNATION: PROGRAMME COORDINATOR	APPROVED BY,  NAME: DATO' IR. JAMALUDIN BIN NON DESIGNATION: DEAN	VERIFIED BY,  NAME: MS. SARMELA DESIGNATION: SENIOR ASST. REGISTRAR EXAMINATION
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