






FACULTY OF ENGINEERING AND BUILT ENVIRONMENT
SEMESTER FINAL EXAMINATION SCHEDULE

DEPARTMENT:	ELECTRICAL AND ELECTRONICS ENGINEERING
PROGRAM NAME:	MASTER OF SCIENCE IN ELECTRICAL AND ELECTRONICS ENGINEERING
INTAKE:	2503
SEMESTER:	MARCH 2025
ACADEMIC SESSION:	10 MAR – 11 JULY 2025

Date/Day	TIME	SUBJECT CODE	SUBJECT NAME	ROOM NO
30.07.2025 WEDNESDAY	09.00 AM -12.00 PM	MEE 1123	ADVANCED VLSI DESIGN	Exam Hall Level 3
31.07.2025 THURSDAY	09.00 AM -12.00 PM	MET 1233	IMAGE PROCESSING	Exam Hall Level 3
01.08.2025 FRIDAY	09.00 AM -12.00 PM	MET 1283	PROJECT MANAGEMENT	Exam Hall Level 3
04.08.2025 MONDAY	09.00 AM -12.00 PM	MET 1113	ADVANCED DIGITAL SIGNAL PROCESSING	Exam Hall Level 3
05.08.2025 TUESDAY	09.00 AM -12.00 PM	MEE 1273	ELECTRIC POWER TRANSMISSION SYSTEM	Exam Hall Level 3

PREPARED BY,  NAME: MS. SUPRAJA SRINIVASAN DESIGNATION: PROGRAMME COORDINATOR	APPROVED BY,  NAME: DATO' IR. JAMALUDIN BIN NON DESIGNATION: DEAN	VERIFIED BY,  NAME: MS. SARMELA DESIGNATION: SENIOR ASST. REGISTRAR EXAMINATION
---	---	---

