

## FACULTY OF ENGINEERING AND BUILT ENVIRONMENT SEMESTER FINAL EXAMINATION SCHEDULE

DEPARTMENT:	ELECTRICAL AND ELECTRONICS ENGINEERING		
PROGRAM NAME:	MASTER OF SCIENCE IN ELECTRICAL AND ELECTRONICS ENGINEERING		
INTAKE:	2503		
SEMESTER:	MARCH 2025		
ACADEMIC SESSION:	10 MAR - 11 JULY 2025		

Date/Day	TIME	SUBJECT CODE	SUBJECT NAME	ROOM NO
30.07.2025	09.00 AM -12.00 PM	MEE 1123	ADVANCED VLSI DESIGN	Exam Hall Level 3
WEDNESDAY	09.00 AIVI -12.00 PIVI	IVIEE 1123	ADVANCED VESI DESIGN	exam nam tever 2
31.07.2025	09.00 AM -12.00 PM	MET 1233	IMAGE PROCESSING	Exam Hall Level 3
THURSDAY	09.00 AWI -12.00 PWI	IVIE 1 1255	IIVIAGE PROCESSING	Exam nam Level 5
01.08.2025	09.00 AM -12.00 PM	MET 1283	PROJECT MANAGEMENT	Exam Hall Level 3
FRIDAY	09.00 AIVI -12.00 PIVI	IVIET 1203	PROJECT IMANAGEMENT	Exam nam Level 5
04.08.2025	09.00 AM -12.00 PM	MET 1113	ADVANCED DIGITAL SIGNAL PROCESSING	Exam Hall Level 3
MONDAY	U9.00 AIVI -12.00 PIVI	INIE I 1113	ADVANCED DIGITAL SIGNAL PROCESSING	Exam Hall Level 3
05.08.2025	09.00 AM -12.00 PM	MEE 1273	ELECTRIC POWER TRANSMISSION SYSTEM	Exam Hall Level 3
TUESDAY	09.00 AIVI -12.00 PIVI	WIEE 12/3	ELECTRIC POWER TRANSIVISSION SYSTEM	Exam nam Level 3

PREPARED BY,

APPROVED BY,

NAME: MS. SUPRAJA SRINIVASAN DESIGNATION: PROGRAMME COORDINATOR

NAME: DATO' IR. JAMALUDIN BIN NON

DESIGNATION: DEAN

VERIFIED BY,

DESIGNATION: SENIOR ASSTREGISTRAR EXAMINATION